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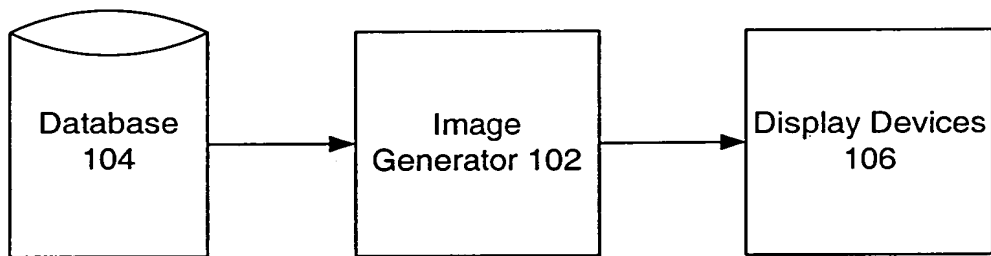


FIG. 1

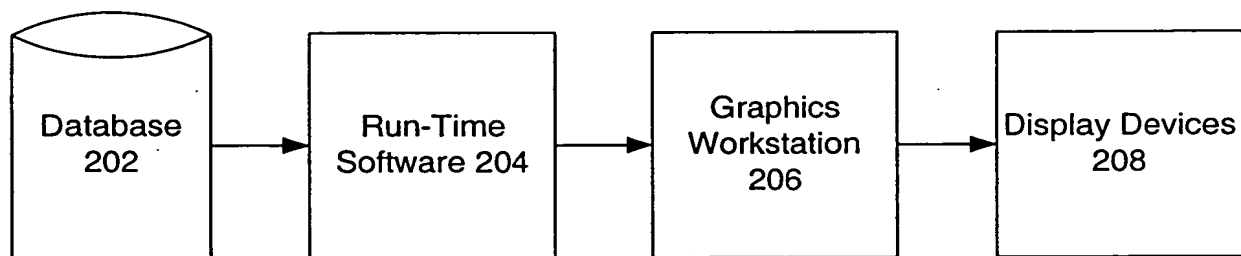
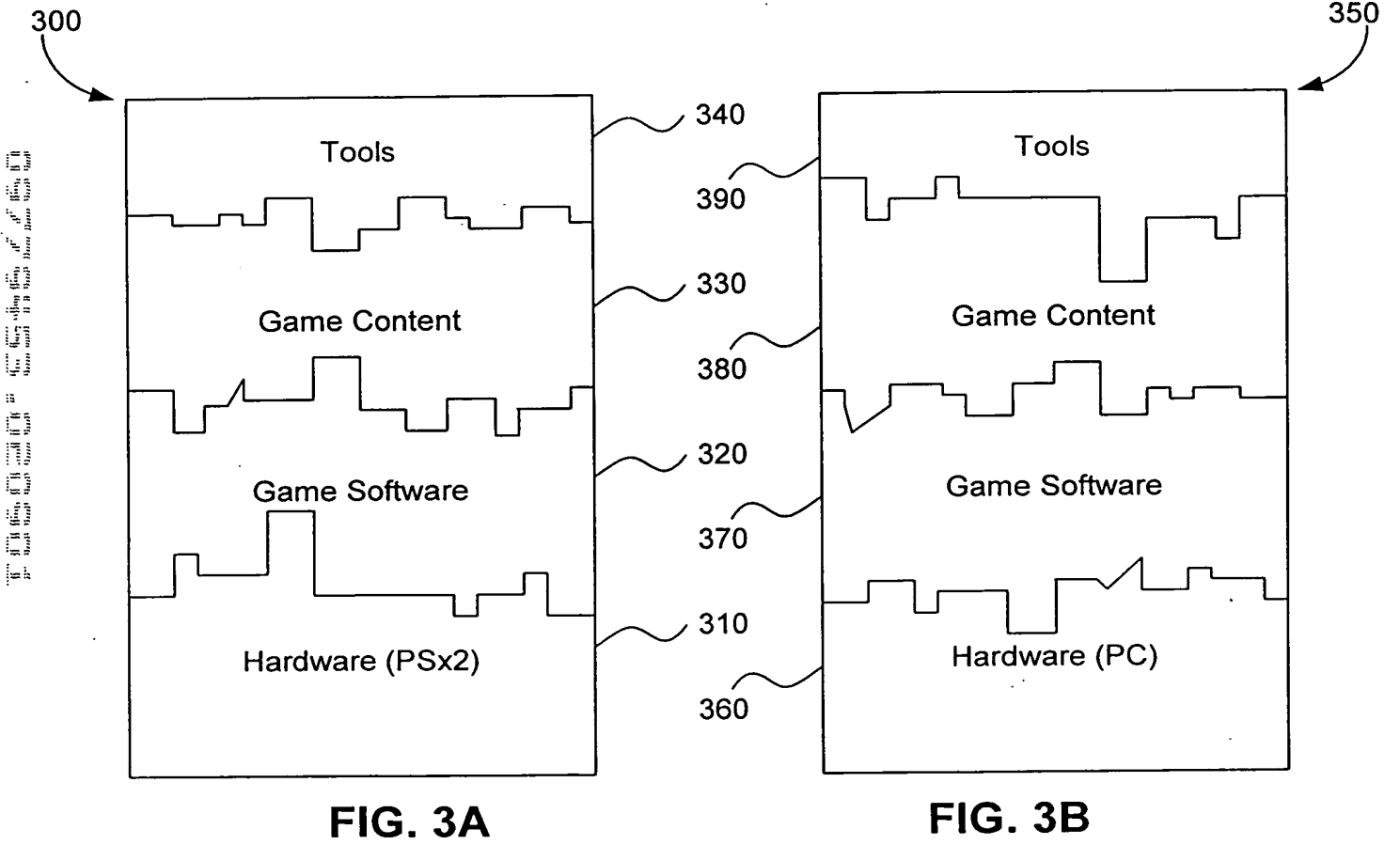


FIG. 2



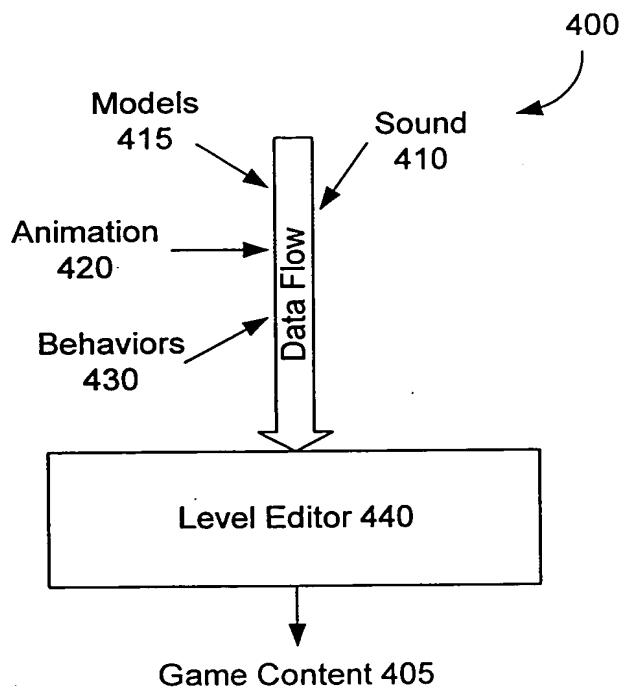


FIG. 4

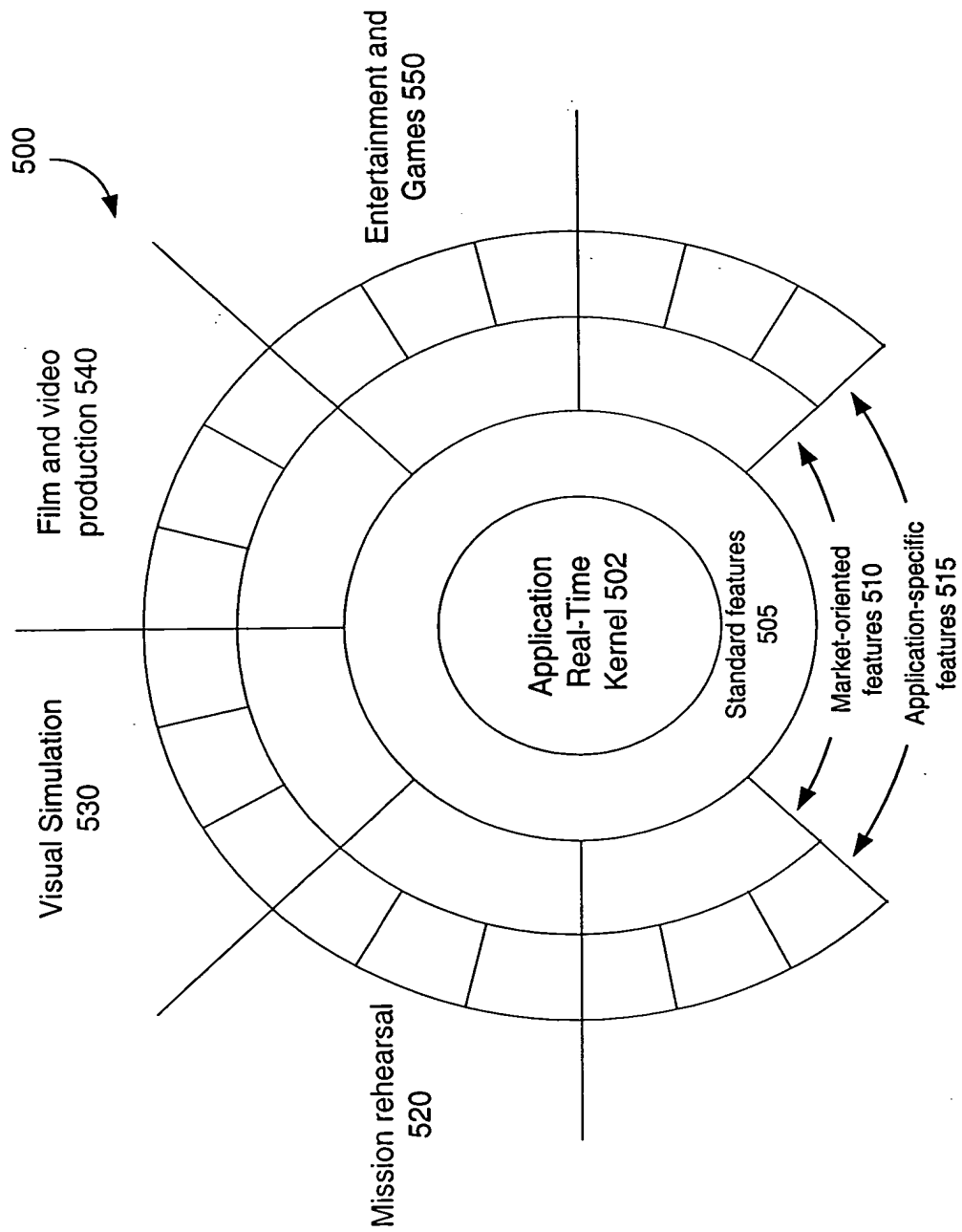


FIG. 5

FIG. 6 is a block diagram of a block 600. The block 600 is a rectangular box containing several components. At the top left, there is a section labeled "Input connection points 605" which contains a table with three rows: "Input 1 name", "Input 2 name", and "Input n name". To the right of this is another table labeled "Output connection points 610" with three rows: "Output 1 type", "Output 2 type", and "Output m type". Arrows point from the output types to a set of three circles labeled "Internal objects 612". Below the output table is a box labeled "Executable content 620" containing the text "Construct (), Destruct (), Initialize (), Evaluate (), ...". To the right of the executable content is a box labeled "Block interface definition 625". At the bottom right, there is a box labeled "Block internal state 615" containing three circles. The entire block is labeled "Block 600" at the top right.

Input connection points
605

Output connection points
610

Internal objects
612

Block 600

Construct (), Destruct (), Initialize (),
Evaluate (), ...

Executable content 620

Block interface
definition 625

Block internal
state 615

FIG. 6

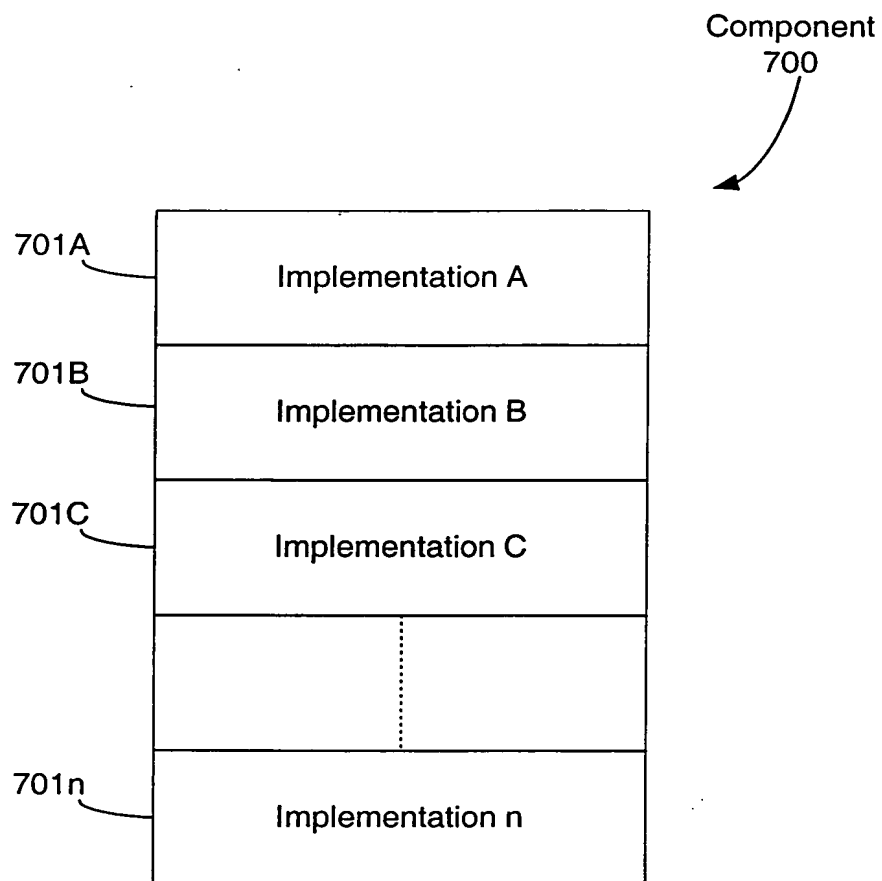


FIG. 7

701A

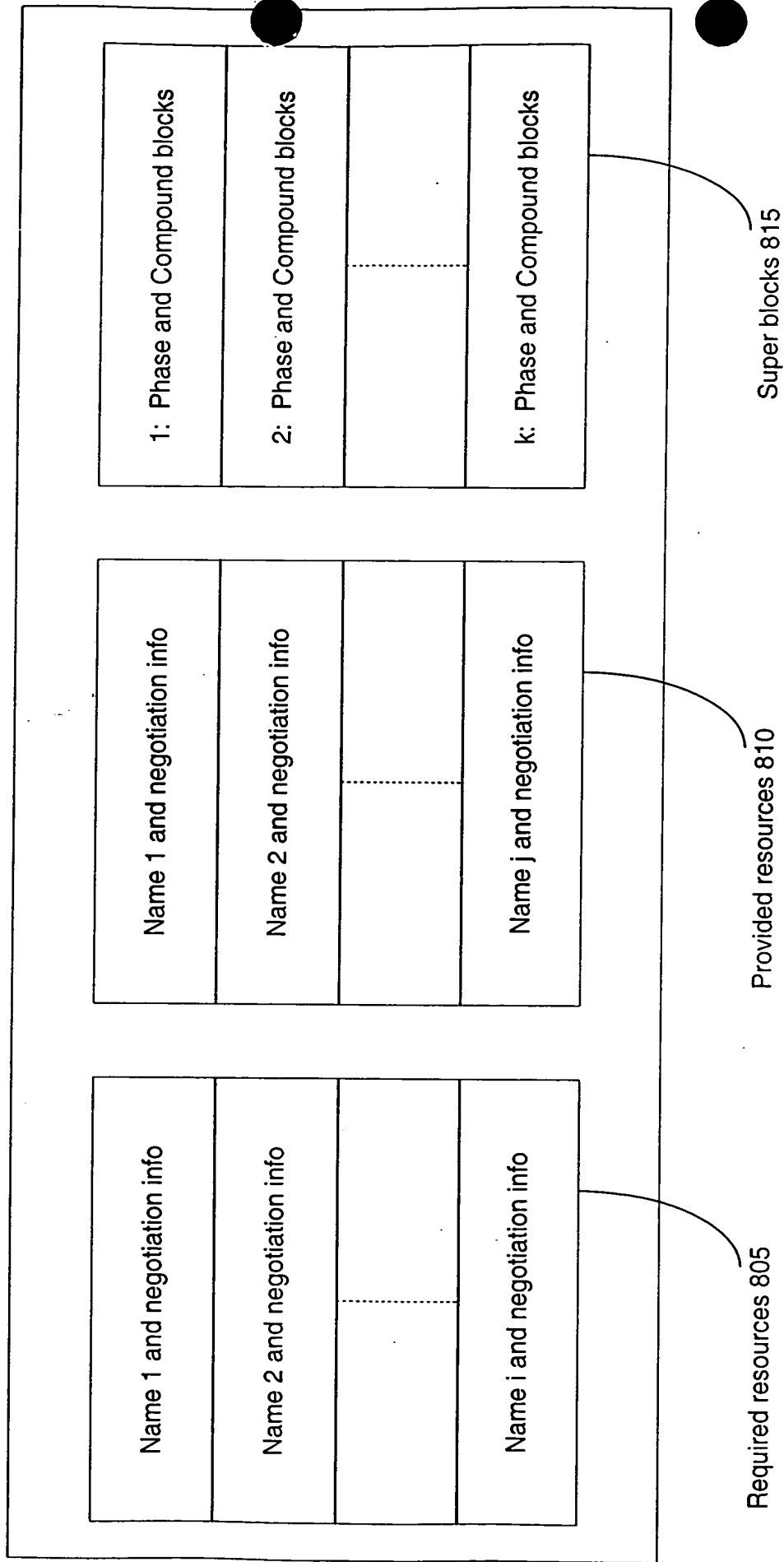


FIG. 8

FIG. 9 is a block diagram of a system 900 for generating a drawing of a structure. The system 900 includes an initialization phase 905, a database paging phase 910, a geometry morphing phase 915, a culling phase 920, and a drawing phase 925. The initialization phase 905 is connected to a set of blocks 930, which are connected to the database paging phase 910. The database paging phase 910 is connected to the geometry morphing phase 915. The geometry morphing phase 915 is connected to a set of blocks 902, which are connected to the culling phase 920. The culling phase 920 is connected to the drawing phase 925. The drawing phase 925 is connected to a set of blocks 900, which are connected to the drawing phase 925.

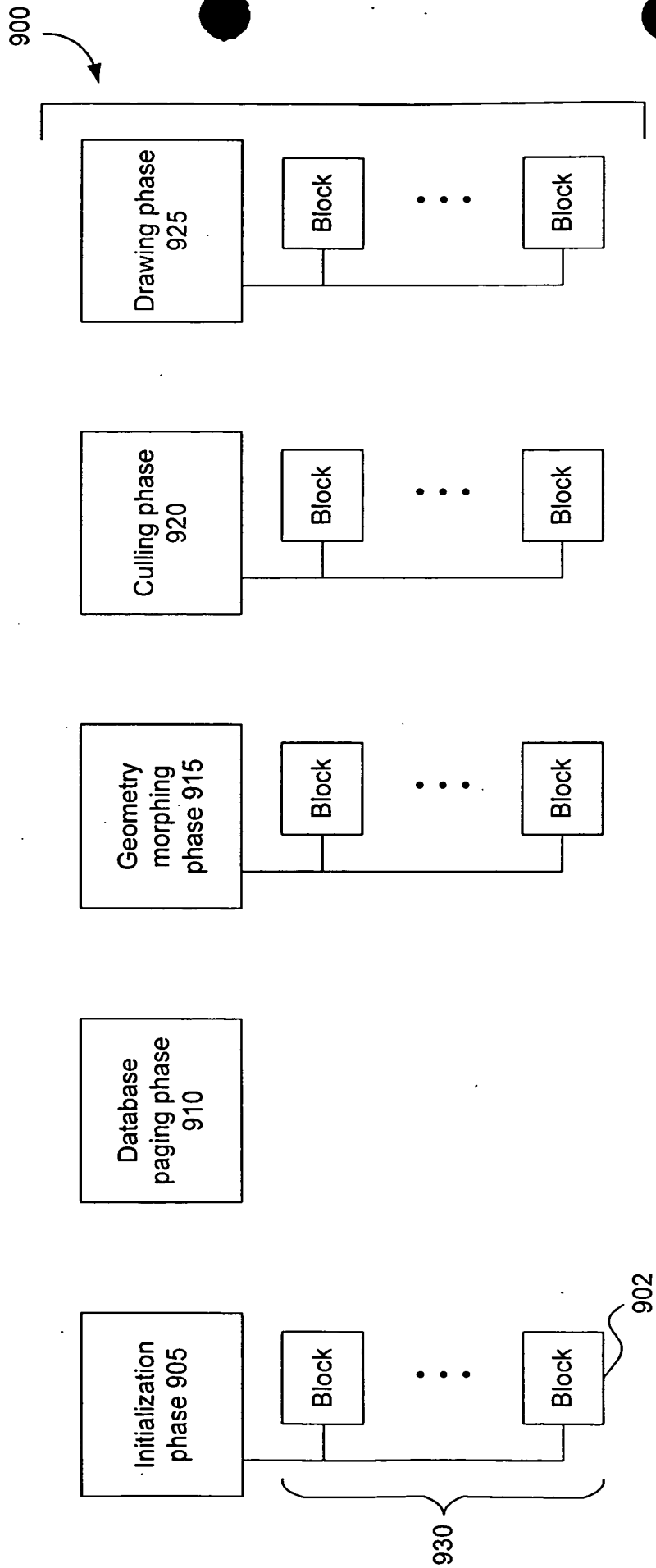


FIG. 9

FIG. 10 is a block diagram of a system 1000 for generating a drawing of a structure.

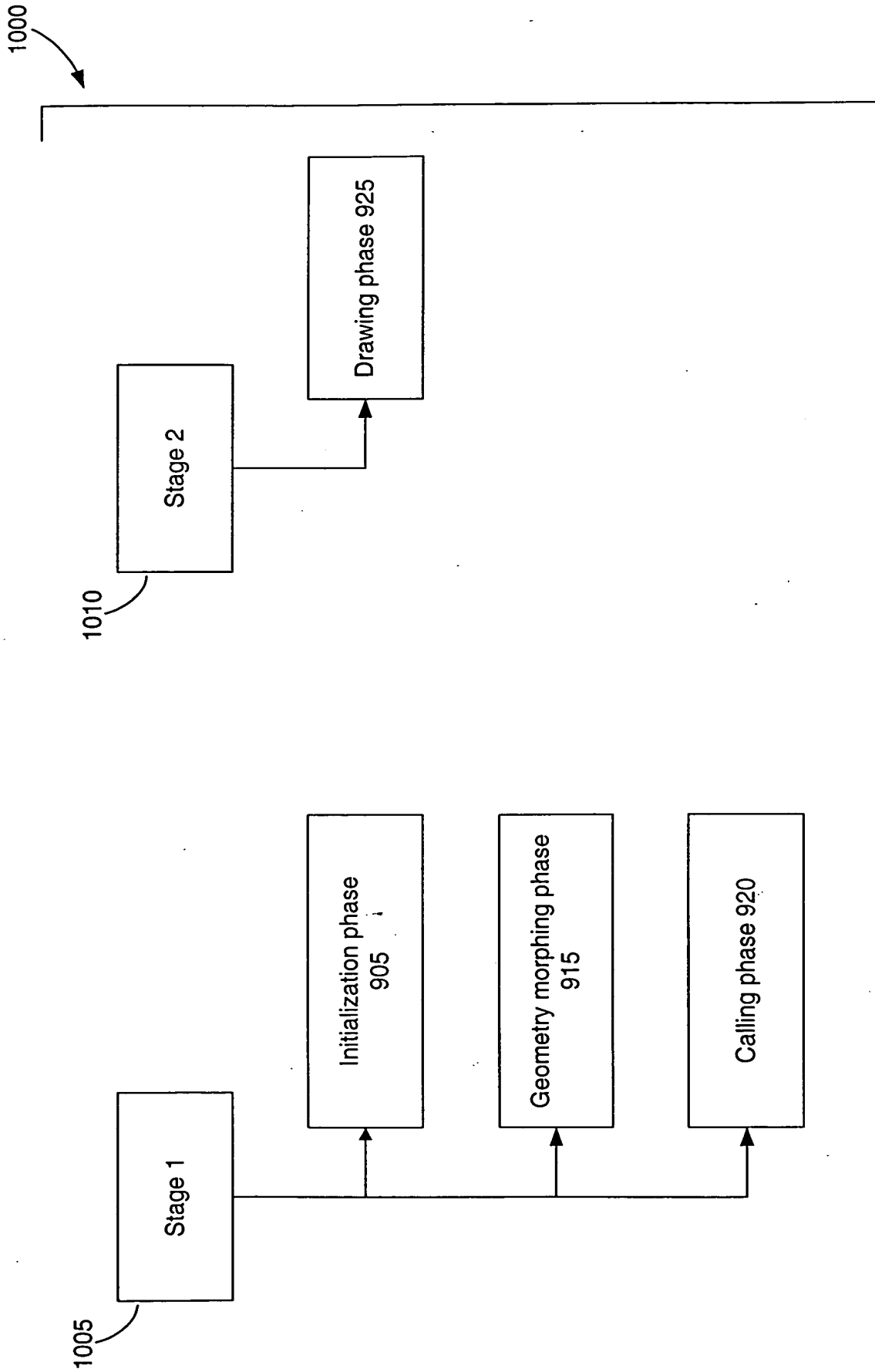


FIG. 10

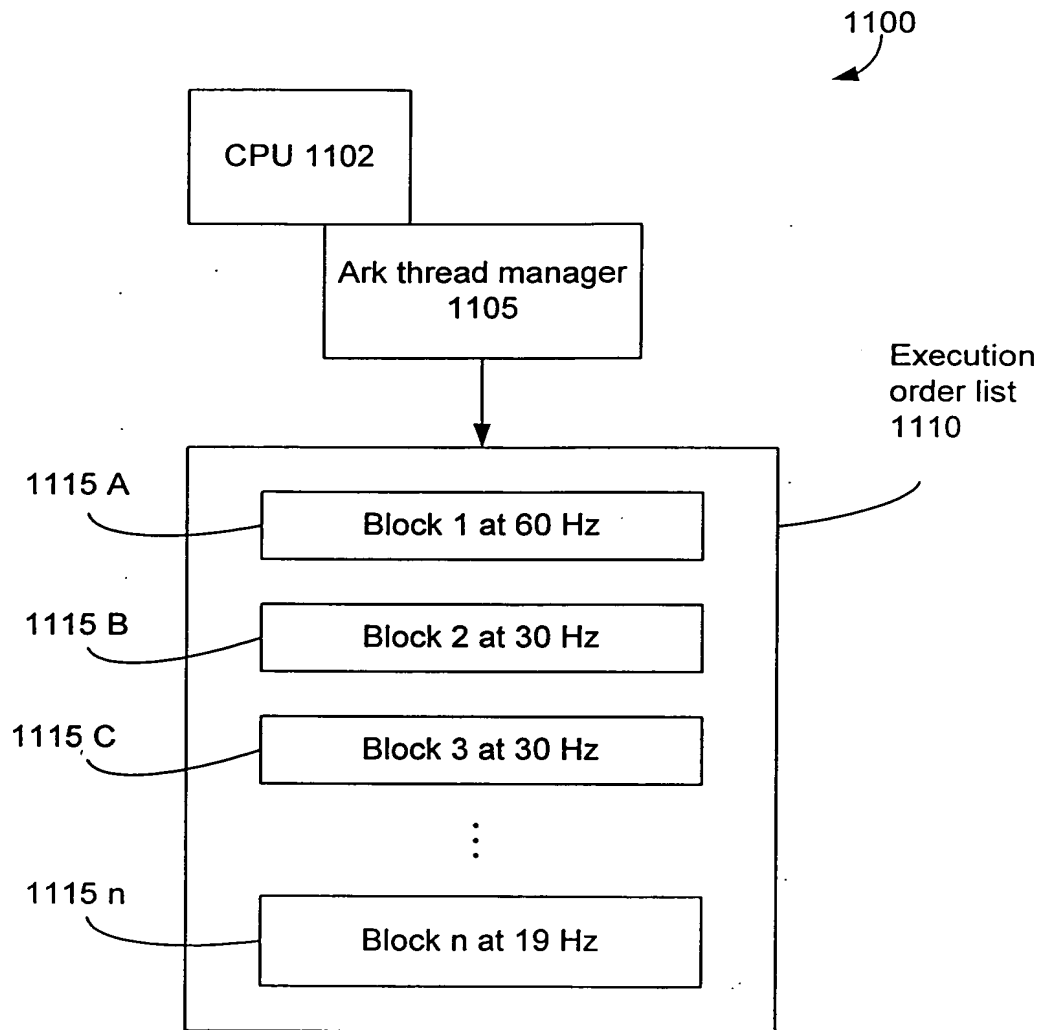


FIG. 11

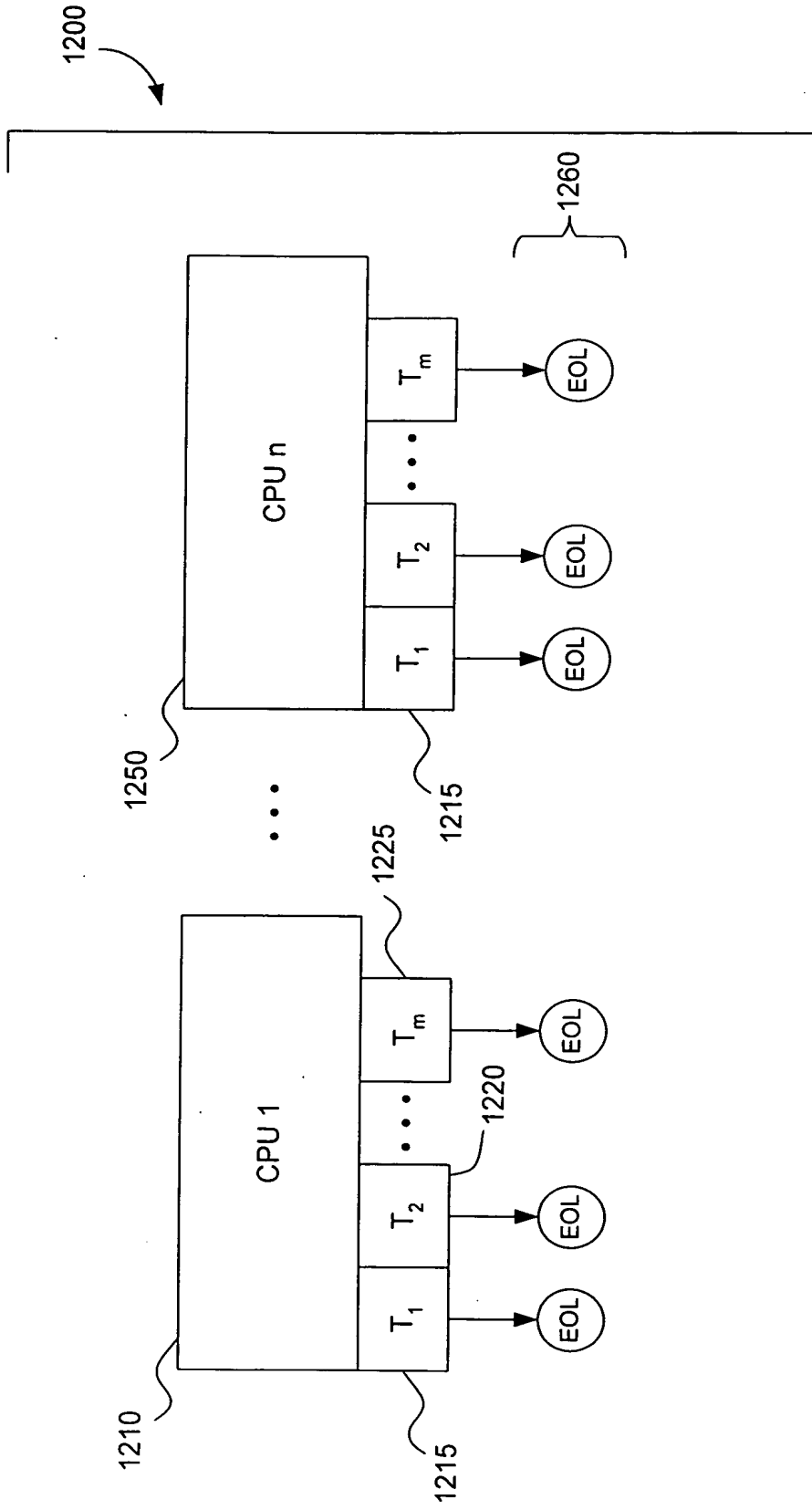


FIG. 12



1300



FIG. 13

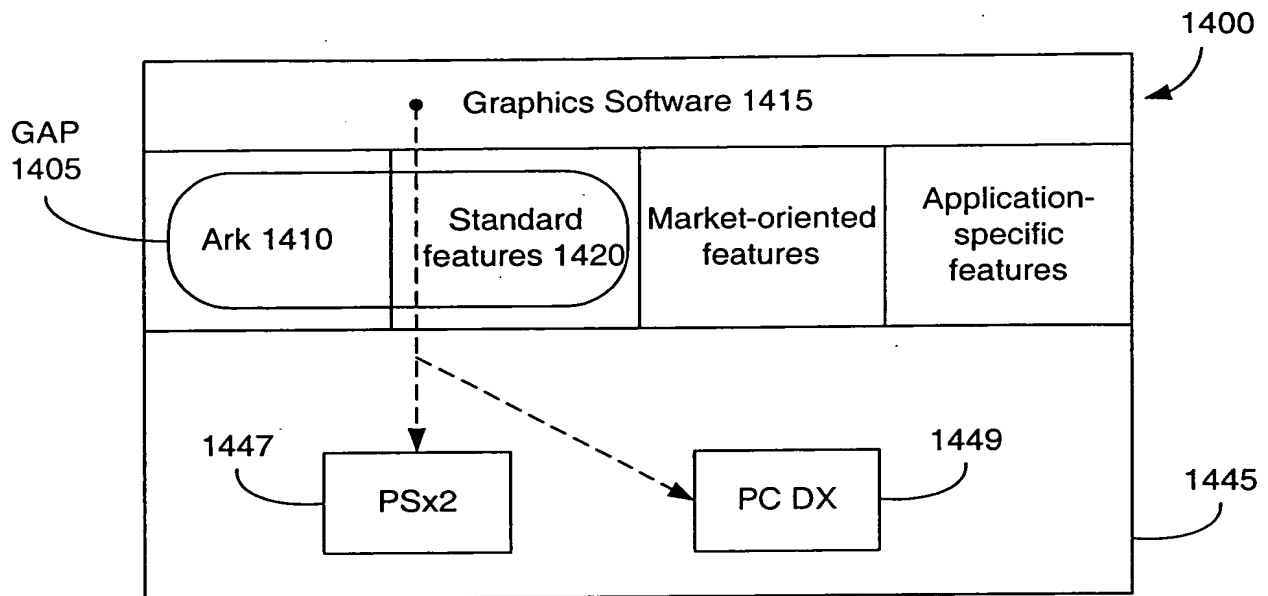


FIG. 14A

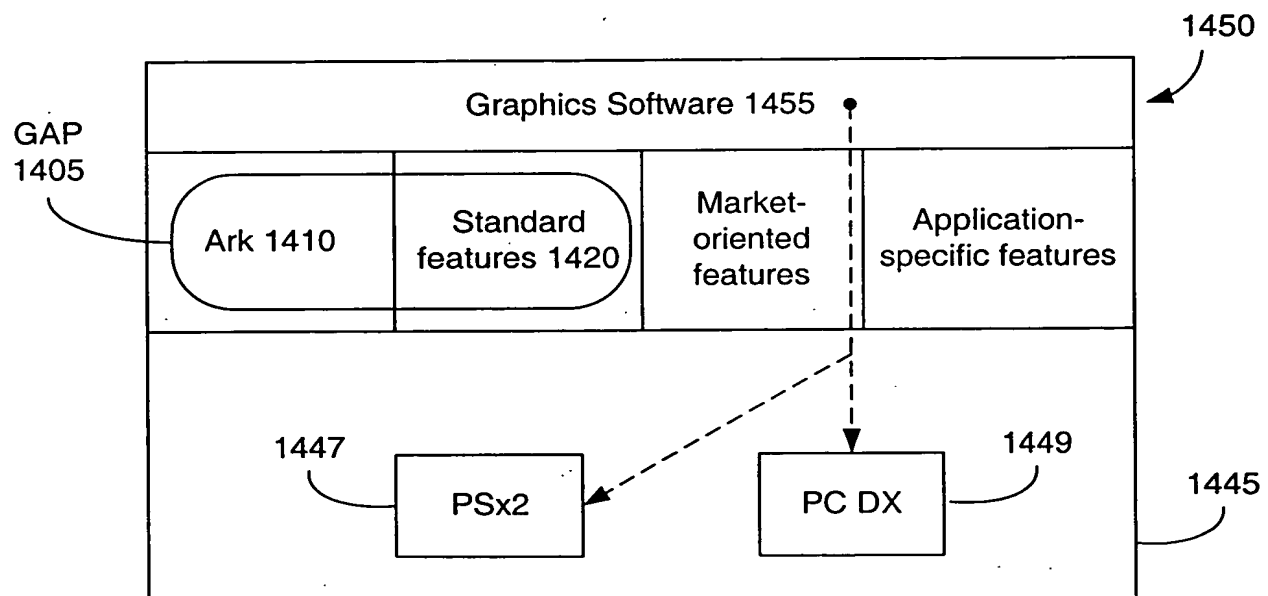


FIG. 14B

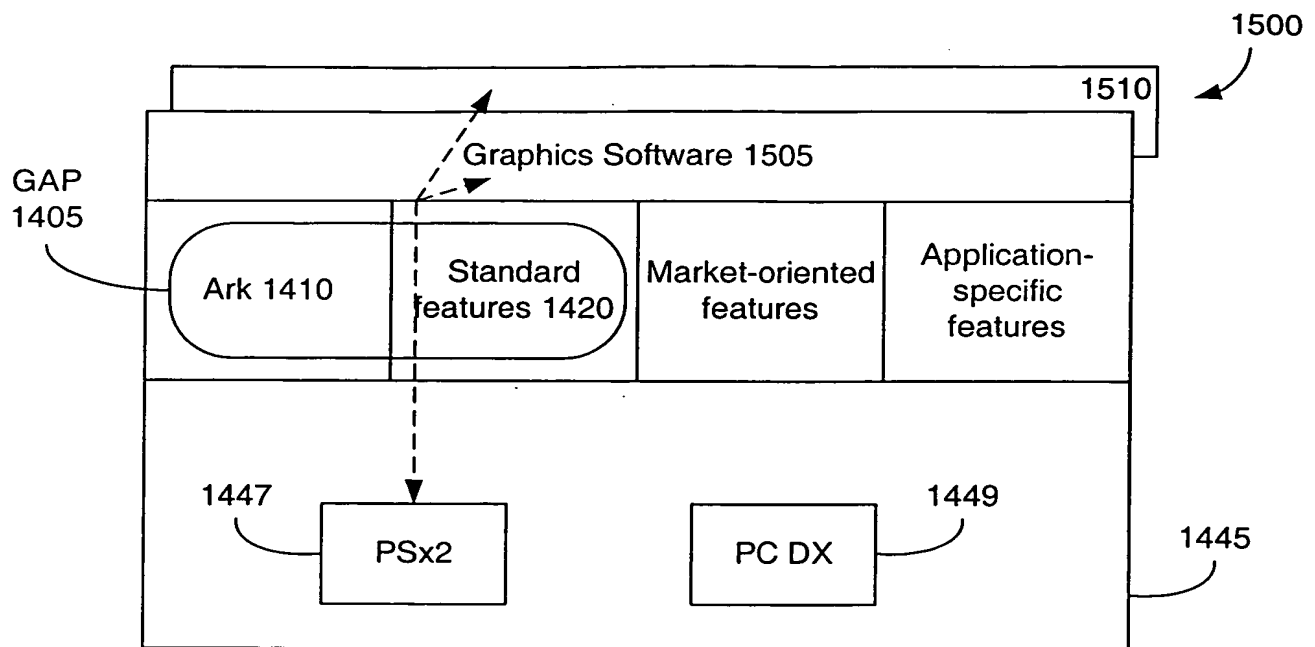


FIG. 15A

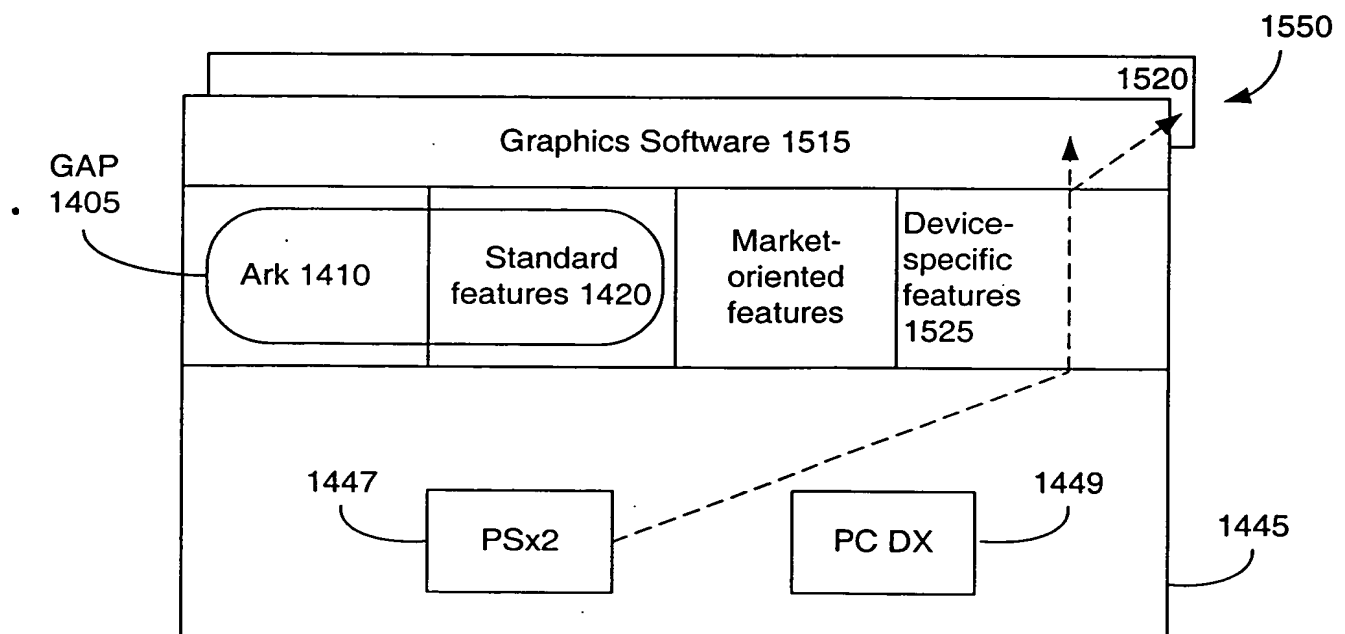


FIG. 15B

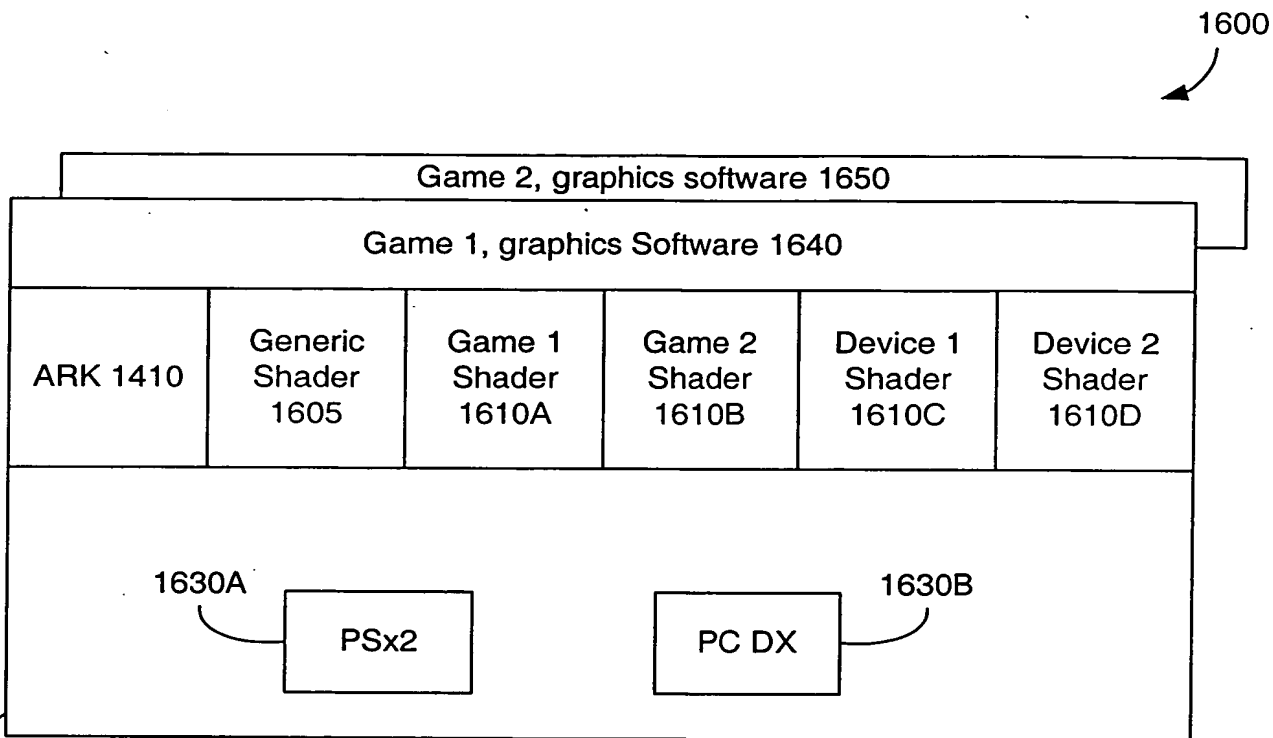


FIG. 16

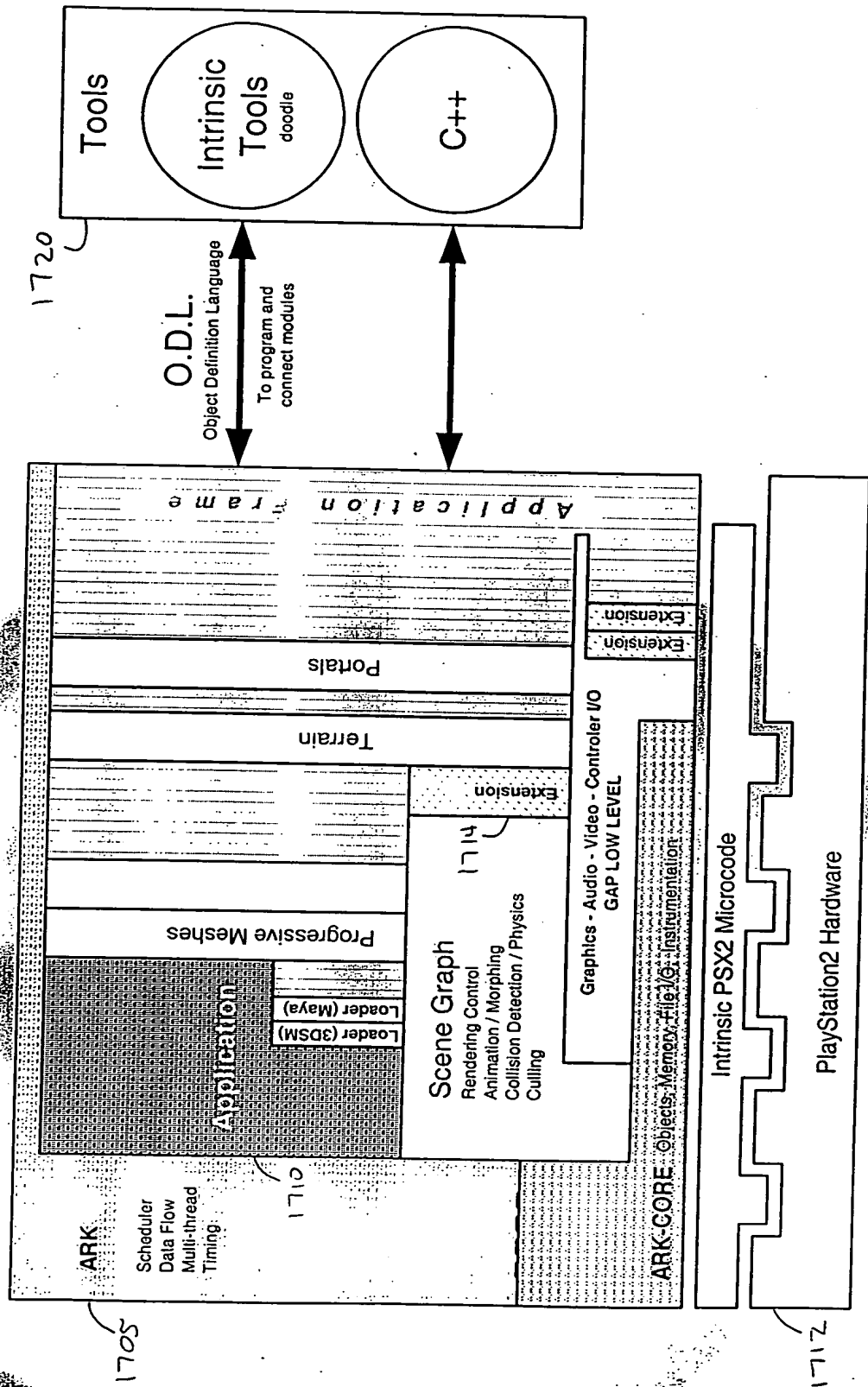


FIG. 17A

1700

1705

1710

1714

1712

1750
↓

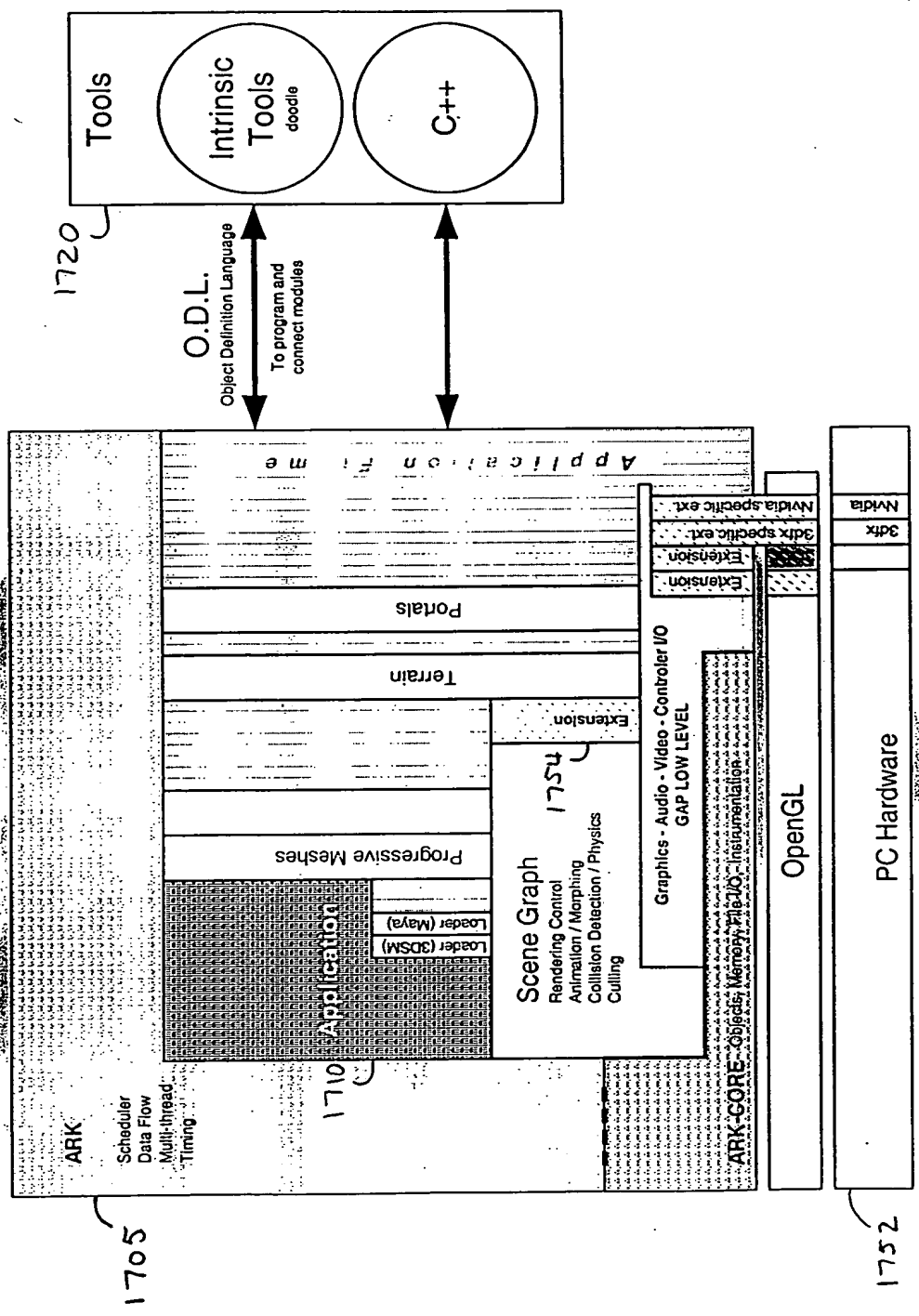


FIG. 17B

FIG. 18 is a block diagram of a system architecture for a graphics processing pipeline. The system is divided into two main sections by a dashed line: a user/application layer on the left and a hardware layer on the right.

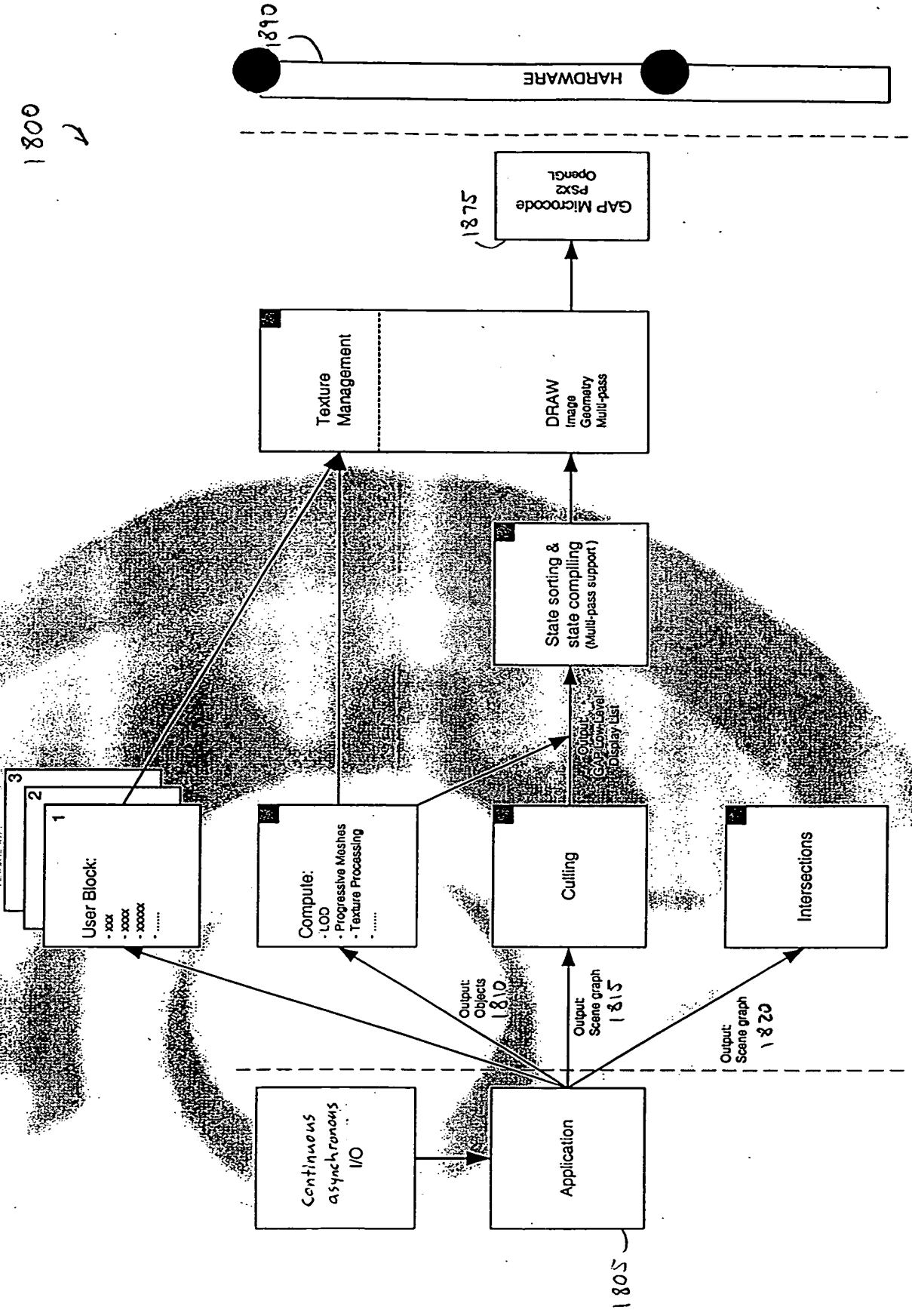


FIG. 18

1900

//

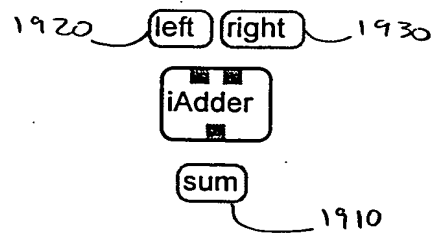
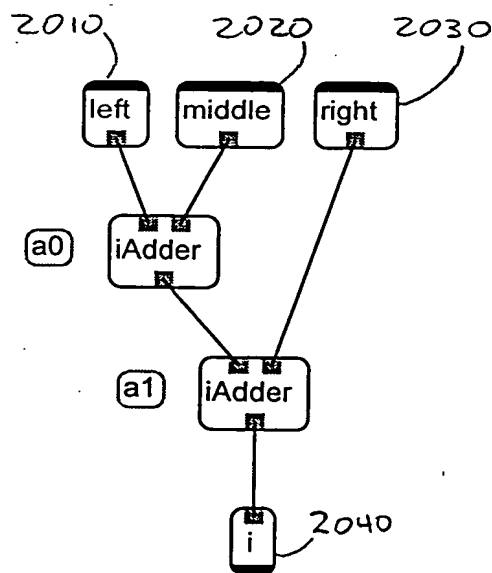


FIG. 19

FIG. 20



2000
↙

FIG. 20

2100

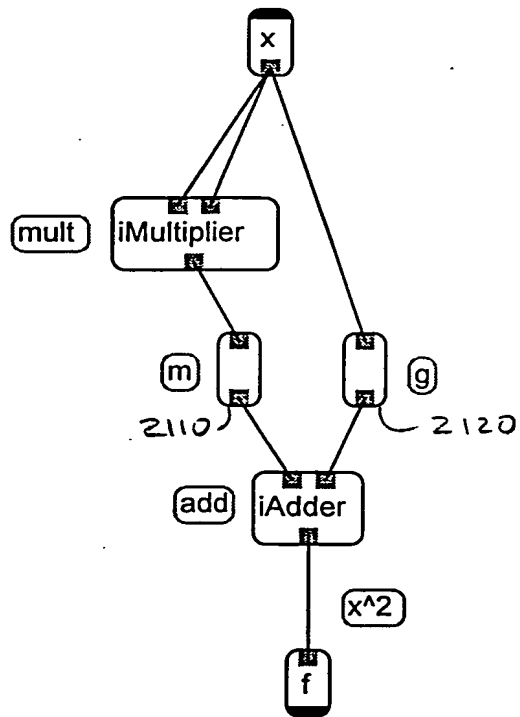
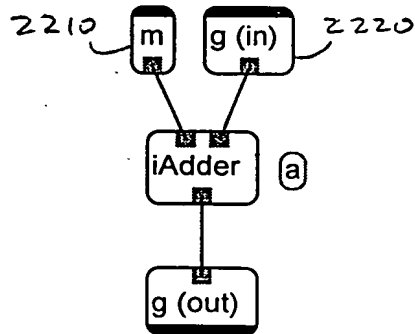


FIG. 21

FIG. 22



2200
↙

FIG. 22

2300

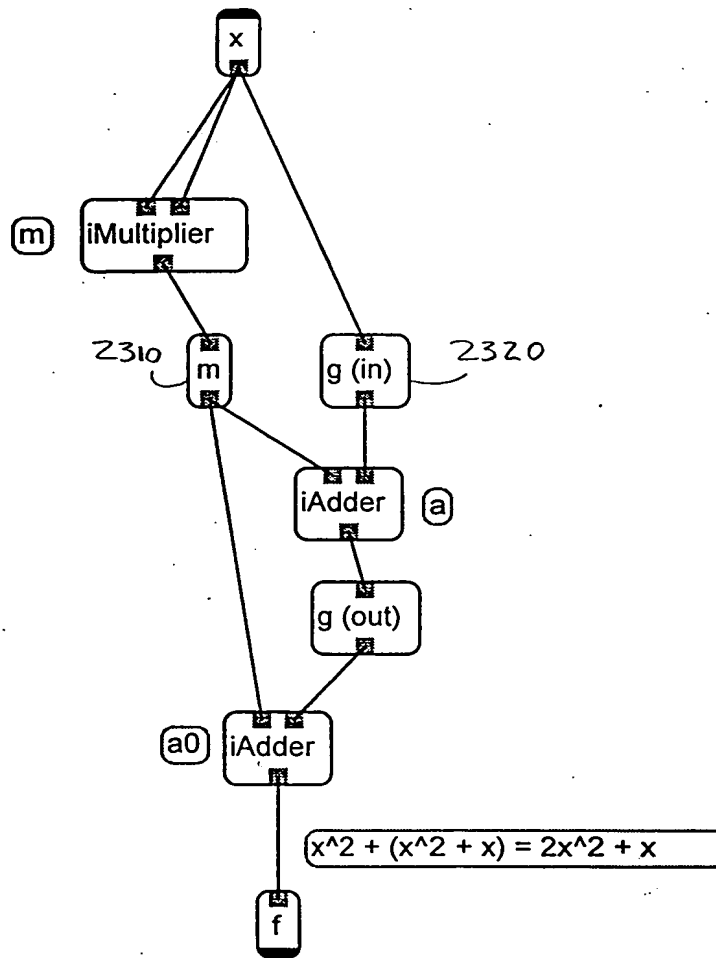


FIG. 23